TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX244F,TC74LCX244FW,TC74LCX244FT,TC74LCX244FK

Low-Voltage Octal Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX244F/FW/FT/FK is a high-performance CMOS octal bus buffer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

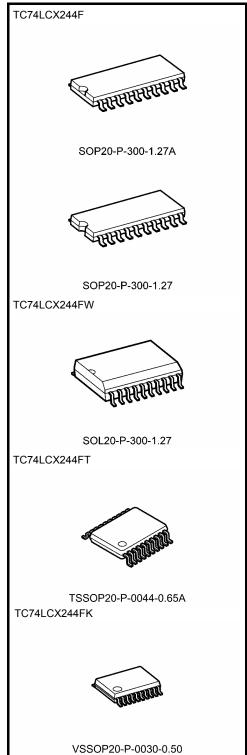
The 74LCX244F/FW/FT is a non-inverting 3-state buffer having two active-low output enables. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: V_{CC} = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 244 type

Note: xxxFW (JEDEC SOP) is not available in Japan.



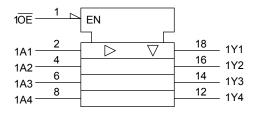
Weight

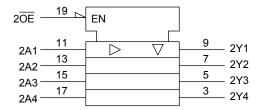
SOP20-P-300-1.27A : 0.22 g (typ.) SOP20-P-300-1.27 : 0.22 g (typ.) SOL20-P-300-1.27 : 0.46 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.)

Pin Assignment (top view)

10E 20 V_{CC} 19 1A1 2OE 2Y4 18 1Y1 1A2 2A4 2Y3 5 16 1Y2 1A3 6 2A3 1Y3 2Y2 7 2A2 1A4 8 13 1Y4 2Y1 12 GND 10 2A1

IEC Logic Symbol





Truth Table

Inp	uts	Outputs
ŌĒ	An	Ουίραιο
L	L	L
L	Н	Н
Н	Х	Z

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	−0.5 to 7.0	V	
DC input voltage	V _{IN}	-0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)		
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V	
Input diode current	Ιικ	-50	mA	
Output diode current	I _{OK}	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	P _D	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	-65 to 150		

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

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Note 2: Output in OFF state

Note 3: High or low state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$



Recommended Operating Conditions (Note 1)

Characteristics	Symbol	Rating	Unit		
Power supply voltage	V _{CC}	2.0 to 3.6			
Power supply voltage	v CC	1.5 to 3.6 (Note 2)	V		
Input voltage	V _{IN}	0 to 5.5	V		
Output voltage	Vout	0 to 5.5 (Note 3)	V		
Output voltage	VOU1	0 to V _{CC} (Note 4)	V		
Output current	I _{OH} /I _{OL}	±24 (Note 5)	mA		
Output current	IOH/IOL	±12 (Note 6)	ША		
Operating temperature	T _{opr}	-40 to 85	°C		
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V		

Note 1: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteri	stics	Symbol	Test Condition			Min	Max	Unit		
					V _{CC} (V)					
Input voltage	H-level	V _{IH}	-	_	2.7 to 3.6	2.0	_	V		
input voitage	L-level	V _{IL}	-	_	2.7 to 3.6	_	0.8	V		
				$I_{OH} = -100 \mu A$	2.7 to 3.6	V _{CC} - 0.2				
	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_			
				I _{OH} = -18 mA	3.0	2.4	_			
Output voltage				I _{OH} = -24 mA	3.0	2.2	_	V		
			V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.7 to 3.6	_	0.2			
				I _{OL} = 12 mA	2.7	_	0.4			
	L-level	V _{OL}			$VOL \qquad VIN = VIH \text{ OI } VIL$ $I_{OL} = 16 \text{ mA}$ $I_{OL} = 24 \text{ mA}$	I _{OL} = 16 mA	3.0	_	0.4	
						I _{OL} = 24 mA	3.0	_	0.55	
Input leakage curren	t	I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μА		
0 -4-44-4# -4-	1	l _{OZ}	V _{IN} = V _{IH} or V _{IL}		0.74-0.0			^		
3-state output oπ-sta	-state output off-state current		V _{OUT} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μА		
Power off leakage cu	rrent	l _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μА		
Ouisseent supply su	rant	1	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	10.0			
Quiescent supply cur	ient	ICC	V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μА		
Increase in I _{CC} per in	nput	Δlcc	V _{IH} = V _{CC} - 0.6		2.7 to 3.6	_	500			

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AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	_	7.5	ns
Tropagation delay time	t _{pHL}	i igure 1, i igure 2	3.3 ± 0.3	1.5	6.5	113
Output anable time	t _{pZL}	Figure 1, Figure 3	2.7	_	9.0	20
Output enable time	t _{pZH}	rigule 1, rigule 3	3.3 ± 0.3	1.5	8.0	ns
Output disable time	t _{pLZ}	Figure 1 Figure 2	2.7	_	8.0	ns
Output disable time	t _{pHZ}	Figure 1, Figure 3	3.3 ± 0.3	1.5	7.0	115
Output to output allow	t _{osLH}	(Alada)	2.7			ne
Output to output skew	t _{osHL}	(Note)	3.3 ± 0.3		1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V_{OL}	V_{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V

Capacitive Characteristics (Ta = 25°C)

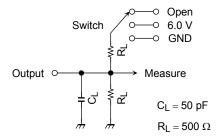
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	3.3	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	6.0 V
t _{pHZ} , t _{pZH}	GND

Figure 1

AC Waveform

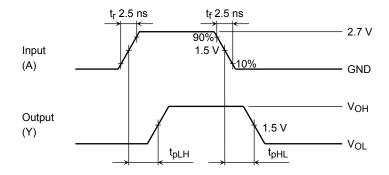


Figure 2 t_{pLH}, t_{pHL}

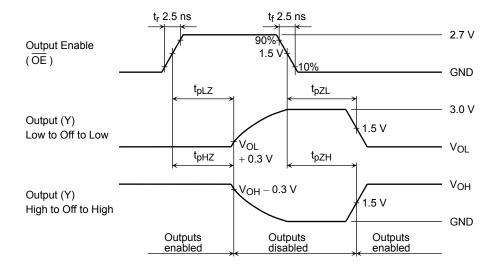
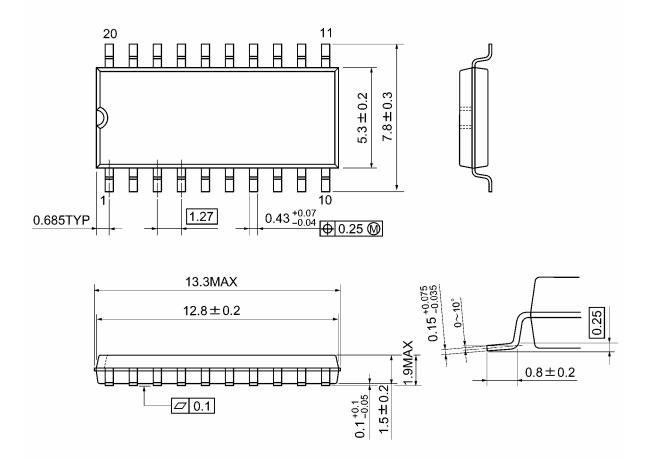


Figure 3 $t_{\text{pLZ}},\,t_{\text{pHZ}},\,t_{\text{pZL}},\,t_{\text{pZH}}$

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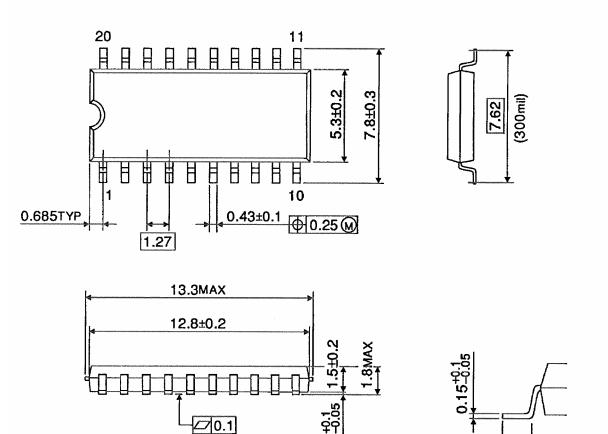
SOP20-P-300-1.27A Unit: mm



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Weight: 0.22 g (typ.)

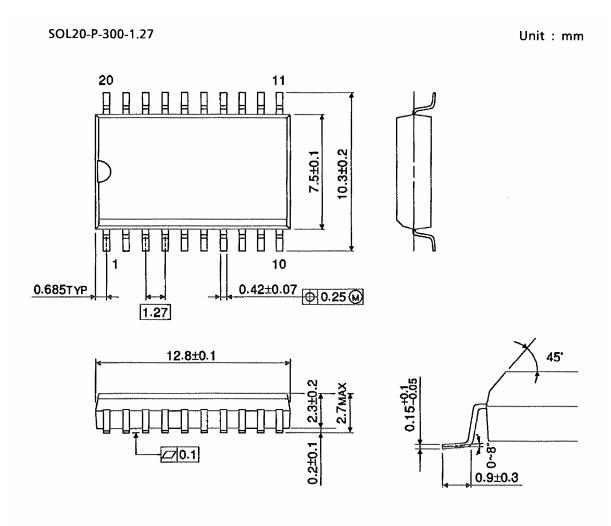
SOP20-P-300-1.27 Unit: mm



Weight: 0.22 g (typ.)

0.8±0.2

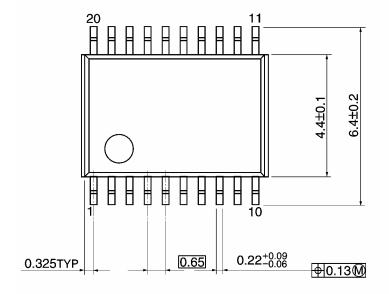
Package Dimensions (Note)

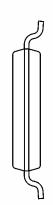


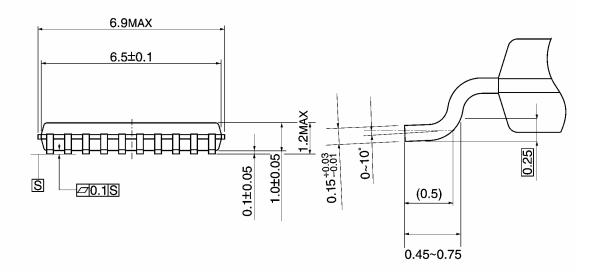
Note: This package is not available in japan.

Weight: 0.46 g (typ.)

TSSOP20-P-0044-0.65A Unit: mm

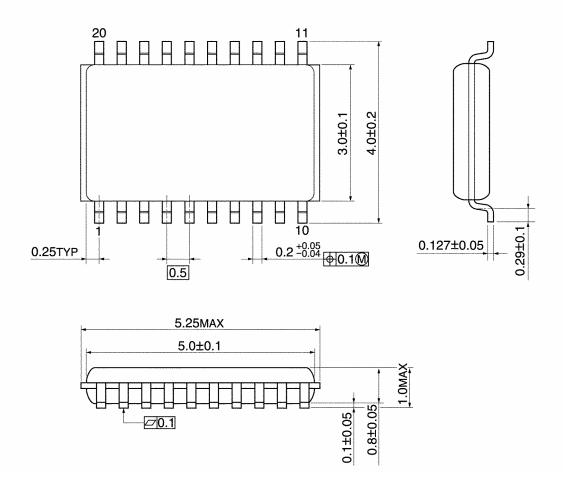






Weight: 0.08 g (typ.)

VSSOP20-P-0030-0.50 Unit: mm



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Weight: 0.03 g (typ.)

Note: Lead (Pb)-Free Packages

SOP20-P-300-1.27A TSSOP20-P-0044-0.65A VSSOP20-P-0030-0.50

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